



INDIAN INSTITUTE OF TECHNOLOGY GANDHINAGAR

Research & Development Office

ADVERTISEMENT NO. SPON/10455/Advt0297 DATED 14.08.2026

Applications are invited for a temporary position of Project Engineer (Device Reliability) at IIT Gandhinagar. The details are as below:

Name of PI	Prof. Nihar Ranjan Mohapatra	Department	EE
Project title	Silicon and Advanced Semiconductor Manufacturing Research and Training Hub (SAMARTH)		
Designation	Project Engineer (Device Reliability)		
Number of positions	One (01)		
Application Link	http://recruitment.iitgn.ac.in/projectstaff/ Applicants must submit the following documents as part of their application: 1. Curriculum Vitae (CV) The CV should clearly include: • Educational qualifications, including degrees, institutions, year of graduation and CGPA/grades. • Details of relevant academic, research and industry projects. • Technical expertise and tool experience. • Professional experience, including internships or full-time employment (if applicable). • Publications, patents, open-source contributions, technical reports, or other notable academic/professional achievements (if applicable). • Awards, scholarships, certifications, and other accomplishments relevant to the position. 2. Statement of Purpose (SOP) (1–2 pages) The Statement of Purpose should address the following: • Your motivation for applying for this position. • Your prior experience and expertise relevant to the role. • How your technical skills, research experience, and career aspirations align with the objectives of the SAMARTH program at IIT Gandhinagar, and how you envision contributing to its research and workforce development activities. • Your long-term career goals and how this position will help you achieve them.		
Last date of application submission	11 th September 2026		
Consolidated Monthly Remuneration including HRA, if applicable	₹60,000 – ₹1,25,000 (based on the qualification and experience)		
Duration of appointment	One year, extendable based on satisfactory performance.		
Eligibility Norms ¹	☒ Institute norms	☐ Funding Agency norms	



Required Qualification*	Ph.D. or equivalent in Electrical/Electronics/Physics/Materials Science discipline with a very good academic record throughout. OR M.Tech./M.S. or equivalent qualification in Electrical/Electronics/Physics/Materials Science discipline with a minimum of 02 years of post-qualification experience in Teaching/Research/Industrial/Design in an organization/Industry of repute. The percentage/grade points with respect to the academic qualifications will be a minimum of 60% or equivalent grade from Graduation onwards and 55% or equivalent grade in class 10th and 12th.
Key Responsibilities	• Conduct research and development on semiconductor device and process reliability. • Investigate device-degradation mechanisms such as bias-temperature instability, hot-carrier degradation, time-dependent dielectric breakdown, self-heating. • Perform wafer-level device characterization using semiconductor parameter analysers, probe stations and other relevant instruments. • Initiate and execute collaborative R&D projects with partner institutions and semiconductor companies. • Prepare technical reports, research proposals, training material, publications and project documentation. • Mentor students and project staff and support the timely execution of SAMARTH workforce-development and research activities. • Support faculty members in organizing and delivering short-term courses, training programs, and workshops conducted periodically by the centre.
Preferred Skills	• Strong understanding of semiconductor-device physics, fabrication processes and device-characterization techniques. • Knowledge of semiconductor reliability mechanisms accelerated stress testing, failure analysis and lifetime modelling. • Hands-on experience with electrical characterization equipment such as semiconductor parameter analysers, probe stations or equivalent systems. • Experience in experimental design, statistical data analysis and interpretation of reliability measurements. • Proficiency in data-analysis and automation tools such as Python, MATLAB or equivalent software. • Ability to develop technical training material and conduct classroom and hands-on laboratory sessions. • Strong problem-solving, technical documentation, communication and project-management skills.



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	• Ability to work independently and collaborate effectively with academic researchers, technical staff and industry professionals. • A strong motivation to contribute to India's semiconductor ecosystem through advanced research, technology development, and innovation.
Contact	Nihar Ranjan Mohapatra Professor, EE IIT Gandhinagar nihar@iitgn.ac.in

*Merely fulfilling minimum eligibility criteria does not entail a call for written test/ interview for the selection. Only candidates shortlisted based on better qualification and quality of relevant experience shall be called for written test/ skill test/interview etc. The authorities reserve all rights, not to call an applicant for selection test or not to fill up a position through this round of selection process without assigning any reason.