INDIAN INSTITUTE OF TECHNOLOGY GANDHINAGAR
Research & Development Office

ADVERTISEMENT NO. SPON/62034/Advt0280 DATED 20.07.2026

Applications are invited for a temporary position of **Post-Doctoral Fellow** at IIT Gandhinagar. The details are as below:

Name of PI	Prof. Joycee Mekie	Department	Electrical Engineering
Project title	Secure and Energy efficient Mixed domain Compute in memory-based AI accelerator Chip for Edge applications		
Designation	Post-Doctoral Fellow		
Number of positions	One		
Application Link	http://recruitment.iitgn.ac.in/projectstaff/		
Last date of application submission	August 16, 2026		
Consolidated Monthly Remuneration	Rs. 72,000 – Rs. 84,000 (Consolidated)		
Duration of appointment	1 year (extendable based on satisfactory performance.)		
Eligibility Norms[1]	☐ Institute norms	☒ Funding Agency norms	
Essential Qualification/Experience as per the norms[1]	Ph.D. (degree awarded/ thesis submitted) in Electrical & Electronics / Electronics / Computer Science and Engineering or related discipline. ☐ The candidate should have a good academic record throughout and the percentage / grade points with respect to the academic qualifications will be a minimum of 60% or equivalent in Graduation and above, and 55% or equivalent grade in 10th and 12th standard.		
Desirable Qualification/Experience	-Expertise VLSI Design, Digital and Analog electronics, Electronic Devices, Computer Architecture, and relevant courses. -Track record of relevant publications in reputed peer-reviewed journals and preferably first author publications in reputed journals (Q1/Q2) and/or A*/A conferences. -Strong foundational knowledge in thermodynamics and kinetics. - Familiarity with VLSI EDA tools is desirable and familiarity with coding. - Ability to conduct independent research - Excellent scientific communication skills.		
Age Limit (If applicable)	___35_____years as on last date of submitting application		
Job Description	The selected candidate will lead the research related to compute in memory and IC design. The candidate is expected to have excellent understanding of Digital & Analog Circuits, VLSI design, etc. and should have a exposure to relevant VLSI tools and techniques. Candidates with excellent programming skills C/C++/Python, Verilog/System Verilog or equivalent, etc. and candidates with interest in computer architecture and having knowledge of system simulators will be		

	given preference. Students with exposure of ADCs, DACs, etc. will be preferable.

For any query, please contact:
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