



इलेक्ट्रॉनिक्स प्रौद्योगिकी सामग्री केंद्र (सी-मेट), हैदराबाद प्रयोगशाला  
**CENTRE FOR MATERIALS FOR ELECTRONICS TECHNOLOGY (C-MET)**  
(Scientific Society, Ministry of Electronics and Information Technology (MeitY), Govt. of India)  
IDA PHASE – III, CHERLAPALLY, HCL (PO), HYDERABAD – 500 051  
Phone: +91-40-27265587, 27267006, 27261658 Web: www.cmet.gov.in

Advt. No.: HD/PN(H.Q.)/SP-102/2026

**Engagement of Project Personnel for the temporary positions of  
“Research Associate (RA)/Junior Research Fellow (JRF)/Project Assistant”**

C-MET invites applications from eligible and highly motivated candidates to fill up the following temporary and time-bound positions under a MeitY-sponsored project entitled “*Organic and Inorganic Materials based Optical Quantum Technology (OIMO-QT) scale-up and manufacturing supply chain in India*”. The position is purely temporary initially for one year and may be extended till the completion of the project (Up to 05.08.2030).

| Sl. No. | Name of the Position                       | No. of Position & Place of Recruitment | Educational Qualifications                                                                                                                                                                                                                                                                                                                                                                                                                                                    | Emoluments* per month                           | Age Limit |
|---------|--------------------------------------------|----------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------|-----------|
| 1.      | Research Associate (RA)/ Project Scientist | 01 (UR)<br>(Pune)                      | PhD in Engineering; Electronics / Electrical / Instrumentation / RF and Microwave Engineering/ Embedded Systems or related field or<br>PhD in Sciences: Physics, Materials Science, Electronics, Photonics, Quantum Technologies, or a related discipline<br><br><b>Desirable:</b> M.Tech/M.E. in Microwave/Electronics/ Microelectronics/VLSI. Minimum 2 years of experience in research and development (as applicable), or advanced engineering roles in relevant domains. | ₹58,000/- + HRA + 1 month basic towards medical | 35-40     |
| 2.      | Junior Research Fellow (JRF)               | 02 (UR)<br>01 each at Hyderabad & Pune | Bachelor's degree: B.Tech /B.E. Electronic/ Electrical / Instrumentation / RF Microwave Engineering/ Communication or Master's degree in Physics /Electronics or equivalent<br><b>Desirable:</b> M.Tech /M.E. in Microwave/Electronics /Microelectronics/VLSI and Microwave/Antenna/ Communication PCB Design and circuit design is preferable.<br><b>Preferred Experience</b><br>• RF / Microwave / Mixed-signal systems                                                     | ₹37,000/- + HRA + 1 month basic towards medical | 28-30     |

|                                                                                                                                                         |                   |                                           |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |                                                 |       |
|---------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------|-------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------|-------|
|                                                                                                                                                         |                   |                                           | <ul style="list-style-type: none"> <li>Antennas / Detector interfaces / High-speed timing / Instrument control / Communication/ Electronic Device Programming, Interfacing, and Wireless Electromagnetics Engineer</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                             |                                                 |       |
| In case of non-availability of NET/GATE qualified candidates, Junior Research Fellow position will be converted as Project Associate as detailed below: |                   |                                           |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |                                                 |       |
|                                                                                                                                                         | Project Associate | 02 (UR)<br><br>1 each at Hyderabad & Pune | Bachelor's degree: B.Tech /B.E. Electronic/ Electrical / Instrumentation / RF Microwave Engineering/ Communication or Master's degree in Physics /Electronics or equivalent<br><b>Desirable:</b> M.Tech/M.E. in Microwave/Electronics/ micro-electronics/VLSI and Microwave/Antenna/ Communication/ PCB Design and circuit design is preferable.<br><b>OR</b><br><b>Preferred Experience</b> <ul style="list-style-type: none"> <li>RF / Microwave / Mixed-signal systems</li> <li>Antennas / Detector interfaces / High-speed timing / Instrument control /Communication / Electronic Device Programming, Interfacing, and Wireless Electromagnetics Engineer</li> </ul> | ₹30,000/- + HRA + 1 month basic towards medical | 28-30 |
| 3.                                                                                                                                                      | Project Assistant | 02 (UR) (Hyderabad)                       | <ul style="list-style-type: none"> <li>Bachelor's degree in science / 3-year Diploma in Engineering &amp; Technology or equivalent</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | ₹20,000/- + HRA + 1 month basic towards medical | 28    |

\* The emoluments of all the positions are due for revision.

Last date for receipt of applications: **16 October 2026 by 17:30 Hrs.**

For application format, educational qualifications, age limit & other important details, please visit our website: [www.cmet.gov.in](http://www.cmet.gov.in)

#### SCREENING CRITERIA:

- PhD in Engineering; Electronics / Electrical / Instrumentation / Microwave/ RF Engineering/Embedded Systems or related field, or PhD in Sciences; Physics, Materials Science, Electronics, Photonics, Quantum Technologies, or a related discipline
- M.Tech/M.E. in Microwave/Electronics/Microelectronics/VLSI. Minimum 2 years of experience in research and development (as applicable) or advanced engineering roles in relevant domains.
- Bachelor's degree: B.Tech /B.E. Electronic/ Electrical / Instrumentation / RF Engineering/ Communication or Master's degree in Physics / Electronics or equivalent

- M.Tech/M.E. in Electronics/microelectronics/ECE/VLSI and Microwave/Antenna PCB Design and circuit design is preferable.
- B.Tech /B.E. (Electronics / Electrical / Communication Engineering): The candidate having microelectronics/ semiconductor/analog devices, solid state physics, RF / Microwave / Electronic Communication/ Mixed-signal systems, Antennas / Detector interfaces / High-speed timing / Instrument control system as one semester course should apply.

#### **NATURE OF WORK:**

- The project activities involve multidisciplinary research encompassing materials synthesis, multilayer PCB design, photonic and quantum device development, optical characterisation, process optimisation, prototype development, and technology demonstration.
- The candidates are required to perform simulation studies, circuit design, Ki Cad, HFSS, ADS, CST RF / Microwave / Mixed-signal systems Antennas / Detector interfaces / High-speed timing / Instrument control , system-level evaluation etc.

#### **IMPORTANT INSTRUCTIONS:**

- Only Indian Nationals need apply.
- Application form can be downloaded from [www.cmet.gov.in](http://www.cmet.gov.in).
- Nature of Position: The engagement on above position is purely temporary initially for one year and may be extended upto project completion as mentioned above.
- **Age Limit:** for each position shall be as per ANRF norms on the closing date of Advt. Age relaxation shall be provided as per Govt. of India rules. However, there is no age relaxation for SC/ST/OBC/PwBD candidates for the Unreserved (UR) position.
- Duly filled-in application with all enclosures Viz., copies of certificates, w. r. t. educational qualification, date of birth, research experience, caste etc., must be sent to "The Centre Head, Centre for Materials for Electronics Technology (C-MET), IDA Phase-III, HCL (PO), Cherlapally, Hyderabad – 500 051" through **Speed Post** only.
- Duly self-attested photocopies of all the documents must be enclosed along with the application form.
- Only a self-attested recent passport size photograph should be pasted in the appropriate place in the application form.
- The last date of receipt of application at C-MET is shall be **16 October 2026**. Applications received after due date for whatever reason shall not be entertained. Incomplete applications are liable to be rejected.
- Write in bold letters '**APPLICATION FOR ENGAGEMENT OF**  
\_\_\_\_\_ (HD/PN(H.Q.)/SP-102/2026)' on TOP of the envelope.
- Candidates working in Govt./Public Sector Undertakings/Autonomous Bodies should bring 'NOC' at the time of interview if called for the same.
- Short-listed candidates shall be called for interview. Interview only at the discretion of the competent authority. Information will be sent to the candidates e-mail address indicated in the application form; therefore, clearly and legibly write your e-mail address and mobile no for communication.
- List of shortlisted candidates called for interview and outcome of the recruitment process will be displayed on our website only. Therefore, candidates may periodically check our website.
- No TA/DA shall be given for attending the Interview.

- Candidates need to produce their original documents for proof of age, educational qualification, research experience, caste and no objection (where applicable), along with supporting documents at the time of interview. In case the certificates/mark sheets etc. have not been collected from colleges/ present employer, a **custodian certificate** from the respective authorities should invariably be produced to the Screening Committee at the time of verification of certificates, failing which the candidate will not be allowed to attend the interview.
- The selected candidates shall not have any legal right to claim his/her regularization/appointment by absorption or otherwise against any regular post or any further contractual engagement on termination of the scheme/project.
- Engagement of selected candidates is subject to his/her being declared medically fit as per the requirement of C-MET.
- C-MET reserves the right to cancel / terminate the candidature at any point of time during the engagement process, if any wrong declaration, misrepresentation or concealment is detected then his / her engagement shall be liable to be terminated without prejudice to any other necessary action will be initiate by the society as per Rule of law. The decision of the competent authority will be final and binding on the candidate. Canvassing in any form and/or bringing any influence, political or otherwise, will be treated as a disqualification for the position applied for.
- Any addendum/corrigendum or extension of the last date for submission of applications or updates will be web-hosted on our website only. Therefore, candidates may periodically check our website.

#### **GENERAL INFORMATION:**

- C-MET, Hyderabad is situated in Cherlapally area and it is connected by Bus Route from Secunderabad, Tarnaka & all over city .
- Further information/query can be obtained over telephone numbers mentioned above from 09:00 hrs to 17:30 hrs on all working days (Monday to Friday) from the Administrative Officer.

Sd/-  
Centre Head

**(See application format below)**



## Centre for Materials for Electronics Technology (C-MET), Hyderabad

Advt. No: HD/PN(H.Q.)/SP-102/2026

### Engagement of Project Personnel

Affix Latest  
Passport Size  
Photograph  
Duly Signed

#### APPLICATION FORM

1. Position applied for :
2. Name in full (in BLOCK letters) :
3. Father's/Mother's/Husband's Name :
4. Date of Birth (Proof to be enclosed):
5. Age as on closing date :
6. Nationality :
7. Address
  - (a) Permanent : \_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_
  - (b) For communication : \_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_
8. Telephone & Mobile No. : 

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|  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |
9. Email id :  
(IN CAPITAL LETTERS)
10. Whether belongs to GEN/SC/ST/OBC(NCL)/PwBD :  
(**Proof to be enclosed**, /strike out whichever is not applicable)

Contd..2/-

::2::

- 11.** Are you related to any of the employees of C-MET:  
If so, provide Name and details of the Employee, Lab where he/she is working & nature of relationship:

- 12.** Educational Qualifications :  
(in chronological order starting with 10<sup>th</sup> /Matric onwards) - *Use extra sheet, if needed.*

| Exam Passed | Year of Passing | University / Board | Subject | Marks | Percentage of Marks |
|-------------|-----------------|--------------------|---------|-------|---------------------|
|             |                 |                    |         |       |                     |
|             |                 |                    |         |       |                     |
|             |                 |                    |         |       |                     |
|             |                 |                    |         |       |                     |

- 13.** Have you passed NET/CSIR-UGC Joint exam/GATE ?

(If 'yes' please indicate)

- i) Name of the exam passed :
- ii) Date of exam passed :
- iii) NET/CSIR-UGC Joint exam/GATE :  
Qualified certificate No.

- 14.** Details of Experience; if any (particulars of all previous and present employment) (starting with the most recent) - *Use separate sheet, if needed.*

| Name of the Organization / Company/ firm | Post/Position Held | Duration |    | Detailed nature of duties performed (Use separate sheet)* |
|------------------------------------------|--------------------|----------|----|-----------------------------------------------------------|
|                                          |                    | From     | To |                                                           |
|                                          |                    |          |    |                                                           |
|                                          |                    |          |    |                                                           |
|                                          |                    |          |    |                                                           |

\* Use separate sheet duly authenticated by your signature  
(Pl. attach self attested copies of certificates)

- 15.** Documents attached in support of experience (Please tick in appropriate box):

- (a) Appointment Letter ☐
- (b) Experience Certificate ☐
- (c) Latest/Last Pay slip ☐

Contd..3/-

**16. Present Position:**

- a. If you are studying, please mention course/subject details.
- b. Details of fellowship/post
- c. Name of the institution/organization in which you are studying/working.

**17. Whether qualifications/experience prescribed for the position applied for are satisfied (if any of the qualification secured by you is considered equivalent to the prescribed, indicate authority thereon):**

**18. References (two references) (Must be indicated clearly)**

|                            |                            |
|----------------------------|----------------------------|
| 1. _____<br>_____<br>_____ | 2. _____<br>_____<br>_____ |
| Mobile No.                 | Mobile No:                 |
| E-mail id:                 | E-mail id:                 |

**19. Any other information you may like to indicate:**

**20. List of Enclosures**

| Sr No | Details |
|-------|---------|
|       |         |
|       |         |
|       |         |

**DECLARATION**

I hereby declare that I have carefully read and fully understood all the instructions and details pertaining to the position being applied by me and all statements made and information furnished in this application are true and complete to the best of my knowledge and belief.

I also fully understand that at any stage it is discovered that an attempt has been made by me to willfully conceal or misrepresent the facts, my candidature may summarily be rejected or my employment terminated. I have gone through the important instructions of the advertisement and agree for it.

Place :

Date :

Signature of the candidate