राष्ट्रीय प्रौद्योगिकी संस्थान, उत्तराखण्ड
NATIONAL INSTITUTE OF TECHNOLOGY, UTTARAKHAND
Phone: 01346-257502/9791051613 **Email:** hariharanm@nituk.ac.in

WALK-IN-INTERVIEW
(Hybrid Mode)

Advt. No.09 **/2026** **Date:**11 **/09/2026**

With reference to the advertisement No. /2026;dated /09/2026,Walk-In-Interview (Hybrid Mode) shall be conducted for the position of Project Associate- I/Project Associate- II/Junior Research Fellow/Research Associate-I/Research Associate-II/Research Associate-III for the project titled "VLSI Design and Implementation of Imagined Speech based Neuroprosthesis: An Application in Healthcare System" funded under Chips to Startup(C2S) Programme- Category-III, MeiTY, Govt. of India under the supervision of Dr. Hariharan Muthusamy, Principal Investigator in the Department of Electronics Engineering, NIT Uttarakhand. The details of post, essential qualifications and fellowship are as under:-

Designation of post	Essential qualifications	Fellowship	Number of post	Tentative date and Time of walk-in interview
Project Associate- I OR	B.E/B.Tech in Electronics and Communication Engineering/Computer Science & Engineering/Electrical and Electronics Engineering/Biomedical Engineering or allied areas	Rs. 25,000/- pm + HRA as per norms		
Project Associate- II **OR**	B.E/B.Tech in Electronics and Communication Engineering/Computer Science & Engineering/Electrical and Electronics Engineering/Biomedical Engineering or allied areas with 2 years' experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organizations and Scientific activities and services	Rs. 28,000/- pm + HRA as per norms		
Junior Research Fellow **OR**	M.E/M.Tech./M.S in Electronics and Communication Engineering/Biomedical Engineering/Digital Signal Processing/ Signal Processing or allied areas with minimum of 1st class in B.E/B.Tech & M.E/M.Tech. In addition GATE/ CSIR-JRF, NET-JRF qualification is mandatory. **Age:** Preferably below 35 Years	Rs. 37,000/- pm + HRA as per norms	02	**Walk-in interview** 30 September, 2026 (Wednesday) at 03.00 PM **Reporting Time** 1.00 PM to 2.30 PM
Research Associate – I **OR** Research Associate – II **OR** Research Associate - III	Ph.D in Microelectronics & VLSI Desig or Signal Processing or M.E/M.Tech having 3 years of research experience with at least one research paper in SCI/SCIE Journal (Specialized in Design and Development of Instrumentation Amplifier for EEG Signal Acquisition or FPGA implementation of EEG Signal Processing, Machine Learning and Deep Learning Algorithms)	Rs. 58,000/- pm + HRA as per norms Rs. 61,000/- pm + HRA as per norms Rs. 67,000/- pm + HRA as per norms		

***The institute may decide the level in which a particular research associate should be placed based on the experience.**

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Job role and skill requirements

- Strong knowledge in Circuit Design for Biomedical Signal Acquisition and EDA Tools
- Strong knowledge in implementation of EEG Signal Processing and Machine/Deep learning algorithms in Python/MATLAB and FPGA
- Good Technical and Communication Skills.

Interested candidates shall appear for walk-in interview along with application form, all originals and self-attested photo-copies of relevant certificates as per the above schedule at Committee Room (C106), Department of Electronics Engineering,

NIT Uttarakhand, Temporary Campus - Govt. ITI, Srinagar Garhwal - 246174. Candidates are urged to send the advance copy of completely filled application form and relevant certificates via email to **hariharanm@nituk.ac.in** in the subject line please specify **"Application for Project Associate – I /Project Associate – II/JRF/Research Associate-I/Research Associate- II/ Research Associate-III position in C2S project"**

General Terms & Conditions:

1. The duration of post will be initially for one year and which may be extended upto the duration of project based on the satisfactory performance. The fellowship may be terminated if performance of candidate is not satisfactory.
2. The post created under the Chips to Startup(C2S) Programme- Category-III, MeiTY, Govt. of India is purely on temporary basis and neither MeiTY nor the Institute would have liability whatsoever for sustaining temporary staff recruited under the above mentioned project, after its completion/termination.
3. Only those candidates who are present before the reporting time on scheduled date will be considered for Interview. **Candidates reporting after the scheduled reporting time for document verification shall NOT be eligible to appear for the Interview.**
4. The Junior Research Fellow/Research Associate-I/II/III with M.Tech/M.E/M.S will have option to register for Ph.D. program of institute; the candidate must meet the essential qualifications and qualify written examination & interview of Ph.D. admission of institute.
5. NIT Uttarakhand has right to cancel the recruitment for the aforesaid post at any time without providing any clarifications/information.
6. No interim correspondence will be entertained and canvassing in any form will lead to disqualification.
7. No TA/DA will be paid for attending the Walk-In-Interview.

Registrar
NIT Uttarakhand